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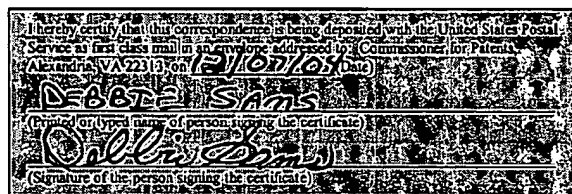
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Theodore F. Vaida  
Serial No.: 10/002,499  
Filed: November 2, 2001  
Title: SELF-REPAIRING INTEGRATED CIRCUIT AND  
METHOD OF OPERATING THE SAME  
Grp./A.U.: 2133  
Examiner: Cynthia H. Britt

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

The Applicant has carefully considered this application in connection with the Examiner's Action mailed September 7, 2004, and respectfully requests reconsideration of this application in view of the following amendment and remarks.